

H/ *Deconcl*
--97. An integrated circuit filter processor as set forth in claim 86, wherein the integrated circuit alterable memory includes an integrated circuit dynamic random access alterable memory dynamically storing operands, said filter processor further comprising a refresh circuit coupled to the integrated circuit alterable memory and refreshing the integrated circuit alterable memory.

IV SUMMARY

It is respectfully submitted that the present application meets the requirements of the patent laws (Title 35 USC), the patent rules (Title 37 CFR), and the Manual of Patent Examining Procedure and that all claims are patentably distinct over the prior art.

IV CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231 on May 3, 1991.

Respectfully submitted,

Dated: May 3, 1991

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